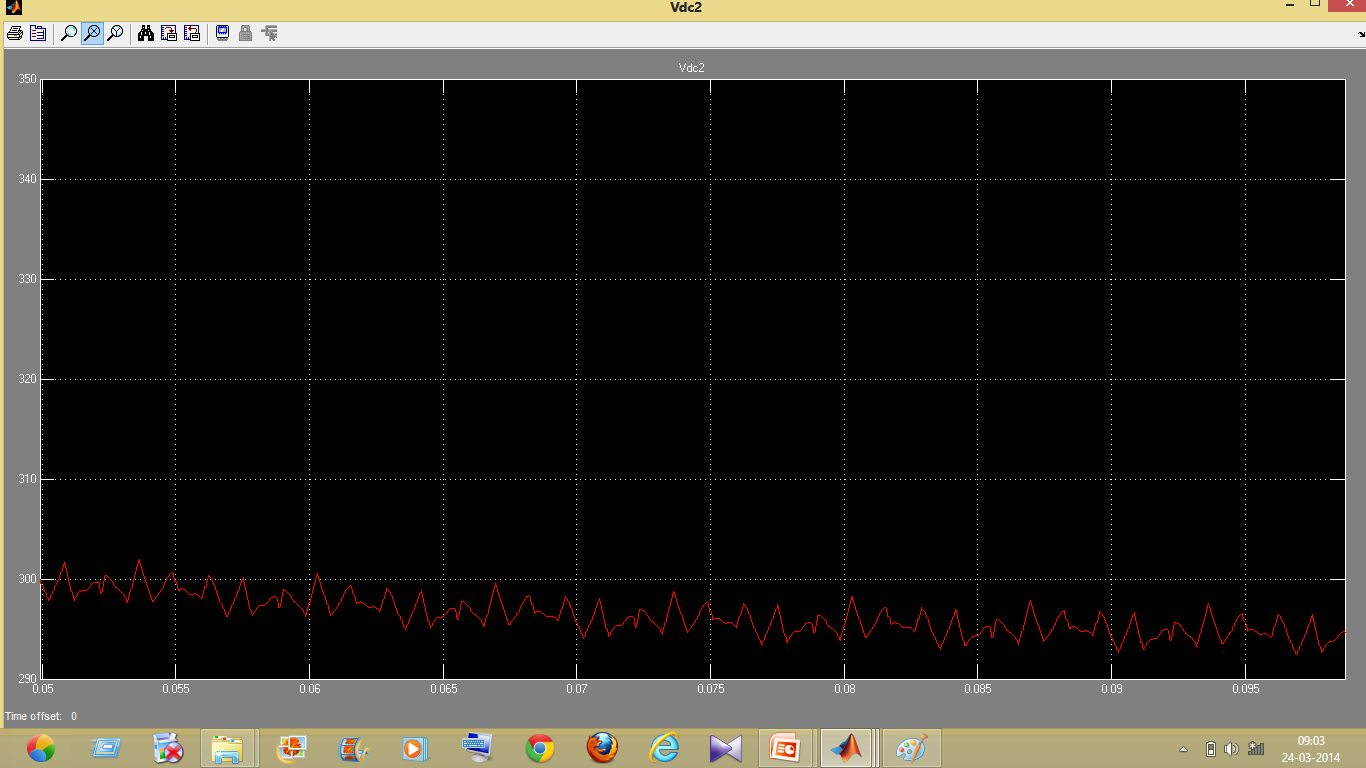
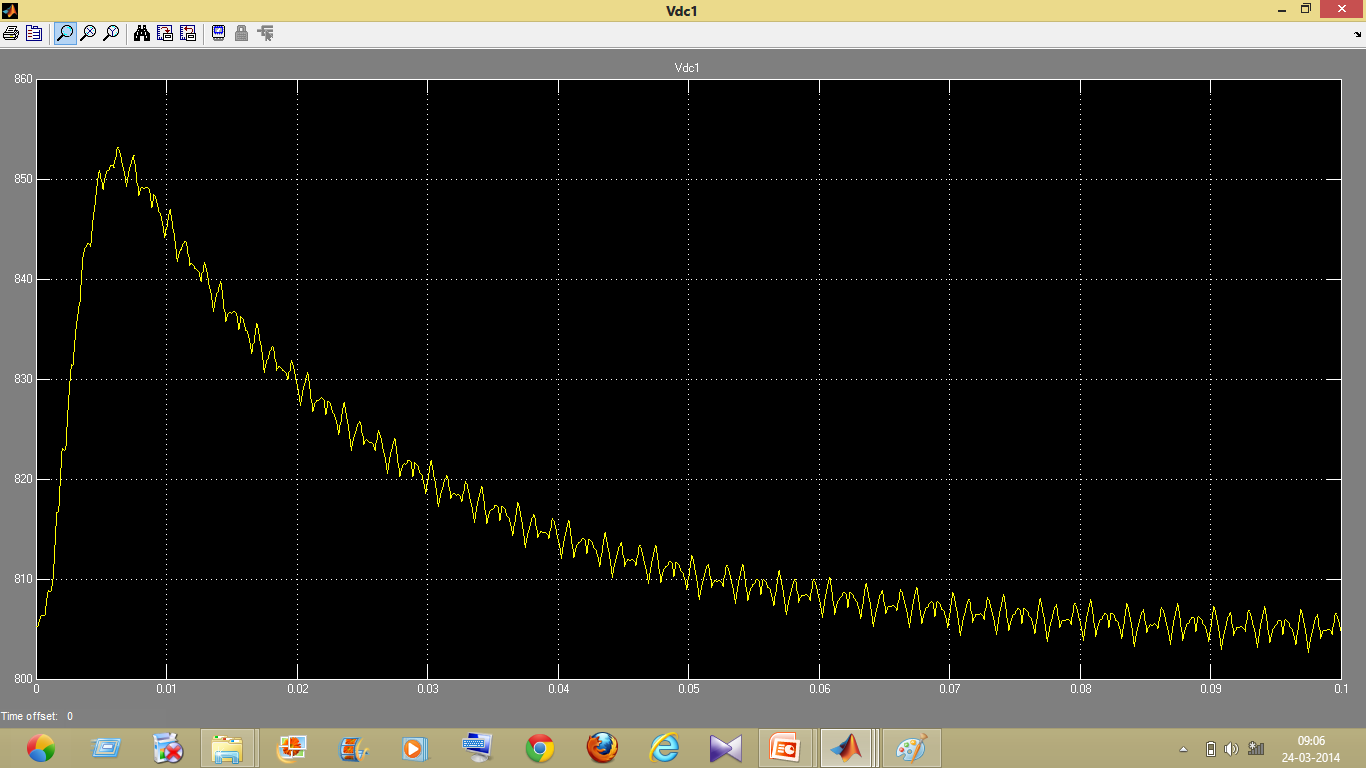


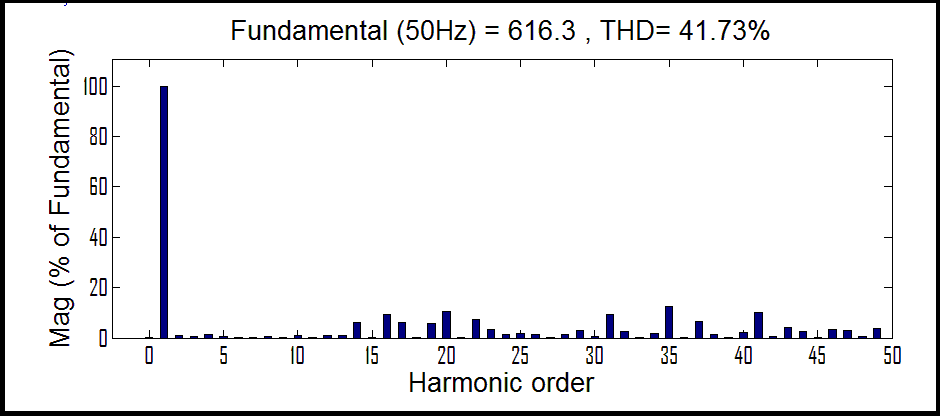
Fig: Primary side voltage (ea) and primary side current (ia)

Vddc1

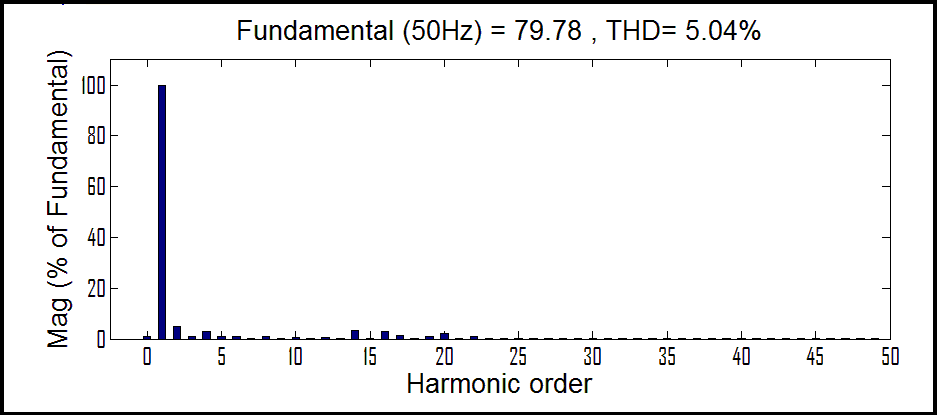


Vdc2





THD Without controller



THD without controller